SECURE-IC
THE SECURITY SCIENCE COMPANY

LABORYZR™
TURNKEY
EMBEDDED SECURITY
EVALUATION LAB

BROCHURE
November 2021 - Version 1.4

Z LABORYZR™

EVALUATE
THE KEY TO A GUARANTEED CERTIFICATION

Driven by a unique approach called PESC (Protect, Evaluate, Service & Certify), Secure-IC positions itself as a partner to support its clients throughout and beyond the IC design process from security requirements to certified solutions in providing them with best-of-breed, end-to-end cybersecurity solutions for embedded systems and cloud connected objects.
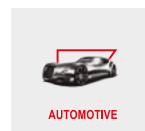
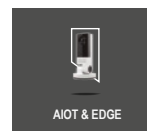### FOR WHOM?

Chipset / Device Vendors

IC Design Houses

Certification Labs

Governmental Agencies

FOR WHICH MARKETS?

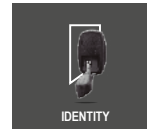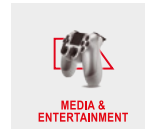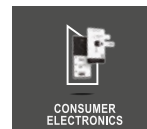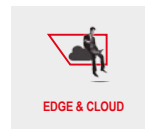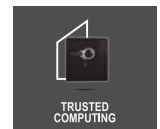|			
AUTOMOTIVE	AIOT & EDGE	eHEALTH	DEFENSE & INDUSTRY
PAYMENTS & TRANSACTIONS	IDENTITY	MEDIA & ENTERTAINMENT	MEMORY & STORAGE
SMART GRID	CONSUMER ELECTRONICS	EDGE & CLOUD	TRUSTED COMPUTING

OUR VISION
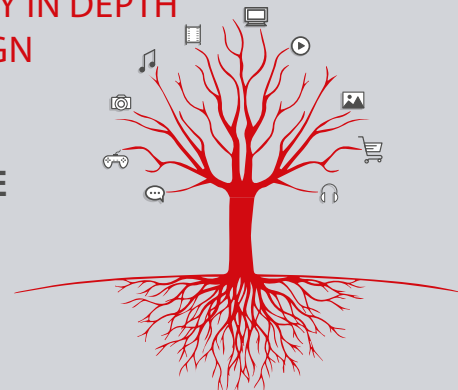### ONE DAY, SECURITY WILL BE WORTH MORE THAN DEVICES

Going forward, there will be more and more interconnected devices or objects in various market verticals, this is what we call the Internet of Things or Internet of Everything.
All those objects being interconnected to the cloud, each and every object could be a threat for the whole network. Therefore, the security of the objects or the devices and their lifecycle management are key.

OUR PESC APPROACH
SECURITY IN DEPTH BY DESIGN

- **P**ROTECT
- **E**VALUATE
- **S**ERVICE &
  CERTIFY

For fully operational security evaluation capabilities, Laboryzr™ provides a complete turnkey laboratory, along with all the knowledge and engineering skills necessary to perform Side-Channel Analysis (SCA) and Fault Injection Analysis (FIA).

PRE-SILICON

POST-SILICON

IP Design · SoC Integration · Back End · **FOUNDRIES** · Product Integration · Certification · Production

Z LABORYZR™

VIRTUALYZR™ · CATALYZR™ · ANALYZR™

LABORYZR™ OFFER INCLUDES 3 TOOLS AND THE RELEVANT EQUIPMENT

VIRTUALYZR™

DESIGN ANALYSIS on VHDL/Verilog/C/ASM Source Code

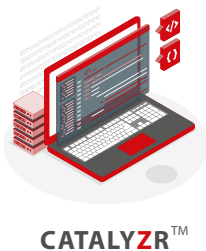**Exclusive feature: Pre-silicon evaluation**

Virtualyzr™ tool aims at assessing the security robustness of an embedded system at an early stage. The platform allows security evaluation at all design levels, namely RTL, Post-Synthesis, Place & Route and Layout. The virtual target can be any hardware design ranging from a single IP to a complete SoC and software programmed in C or ASM.

ANALYZR™

DEVICE ANALYSIS on ASIC, FPGA or end-product

Most advanced post-silicon evaluation tool

Analyzr™ tool enables security evaluation on real physical chip/boards after foundry tape-out. It includes all the necessary equipment to perform efficiently SCA measurements and fault injections (FIA). The platform is user-friendly and generates a high-end security report. Challenge and reinforce your security design. Using FIPS-140 (2/3) and ISO-7825 methodologies, you can enter the certification process with confidence.

CATALYZR™

EMBEDDED SOFTWARE SECURITY EVALUATION TOOL

Build a secure software implementation

Catalyzr™ is a software tool that aims at assessing the security of a software implementation. It provides an end-to-end workflow that starts with design input and ends with the generation of a security report. The design input is a pure software code that can be written in C or Assembly (ASM). Additionally to Side-Channel Analysis, Catalyzr™ allows to perform Fault Injection on software code to detect potential vulnerability.

LABORYZR™ PROVIDES YOU WITH EVALUATION EQUIPMENT, KNOWLEDGE AND SKILLS

VALUE OF LABORYZR™

- **For Designers and for Security testing laboratories**
- **End-to-end security evaluation of embedded systems: from IP design to Production**

- **A complete set of Analysis tools:**
  - Virtualyzr™: Pre-silicon evaluation – Secure-IC Exclusive,
  - Analyzr™: Post-silicon evaluation – Advanced tools,
  - Catalyzr™: Build a secure software implementation.

- **Comprehensive set of equipments**
- **In-depth knowledge and skills transfer**

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