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# **5G Small Cell O-DU Platform**

## **SC-60**

### Platform Briefing

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VERSION 1.2

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## Introduction

O-RAN Alliance has defined reference distributed architecture to enable next generation RAN infrastructure, consisting of DU and RU. CIG has developed a series of hardware platform specially targeted for 5G small cell deployment for indoor 5G coverage and capacity enhancement. The platform is fully programmable based on general purpose ARM Processor and FPGA. A 3-tier architecture is adopted to carry out the gNB functionality, i.e. SC-60 O-DU, SC-70x O-RU and SC-640 O-Bridge. The number of each component can be flexibly configured to meet various application demands.

The O-DU is the center of the platform. Partial or all 5G air interface functions can be implemented within the O-DU, including L1, L2 and L3, according to the networking requirements.



## Highlights

- 3GPP and O-RAN compliant
- 5G evolvable, LTE compatible
- Cost effective high performance 16-core NXP ARM Processor and FPGA
- Flexible capacity with configurable processing modules
- Flexible functions with optional CU accommodation and various low layer split
- Compact, easy for installation and maintenance

## Specification

<b>Dimension (W x H x D)</b>	483mm x 43.6mm x 430mm
<b>Front haul Interfaces (FH-x)</b>	8 * 25Gbps (2 * DU modules), SFP28
<b>Mid haul Interface (DU-x)</b>	8 * 10Gbps (2 * DU modules), SFP+
<b>Backhaul Interfaces (CU-x)</b>	4 * 10Gbps (2 * DU modules), SFP+
<b>Power Supply</b>	DC: -48V
<b>Power Consumption</b>	320W (Typical full loading)
<b>Weight</b>	≤7KG

## Features

<b>Radio access technology</b>	<ul style="list-style-type: none"> <li>- 5G NR support</li> <li>- Sub6G and mmWave (future releases) support</li> <li>- 3GPP Option 2 high layer split, CU optional</li> <li>- O-RAN Option 7-2x low layer split, eCPRI support</li> </ul>	
<b>Performance and Capacity</b>  <i>Note: Performance and capacity numbers depend on 3<sup>rd</sup> party software optimization</i>	1 or 2 DU modules	Each DU module supporting: <ul style="list-style-type: none"> <li>- 2 * 100MHz 4T4R (4 DL / 2 UL layers) or 4 * 100MHz 2T2R (2 DL / 2 UL layers) in FR1</li> <li>- (Future releases) 4 * 100MHz 4T4R (4 DL / 2 UL layers) in FR1</li> <li>- (Future releases) 2 * 400MHz 2T2R (2 DL / 2 UL layers) in FR2</li> <li>- Up to 64 O-RU with O-bridge/O-RU cascading</li> </ul>
	Cell performance	<ul style="list-style-type: none"> <li>- 1.8Gbps DL / 0.6Gbps UL peak data rate per 4T4R (4 DL / 2 UL layers) cell</li> <li>- Up to 256 active users per cell</li> </ul>
<b>Timing</b>	<ul style="list-style-type: none"> <li>- Timing from GPS/Beidou/IEEE 1588v2</li> <li>- Providing timing for O-RU with SyncE and IEEE 1588v2</li> <li>- 10MHz and 1PPS clock out</li> </ul>	
<b>Interfaces</b>	1 or 2 DU modules, each supporting: <ul style="list-style-type: none"> <li>- 4 * 25Gbps SFP28 for fronthaul (to O-Bridge, or O-RU while downgraded to 10G)</li> <li>- 4 * 10Gbps SFP+ for midhaul (to external CU)</li> <li>- 2 * 10Gbps SFP+ for backhaul (to Core, provided together with CU module)</li> <li>- Management and timing interfaces</li> </ul>	
<b>Hardware</b>	<ul style="list-style-type: none"> <li>- Each DU module: 2*NXP LX2160A 16C 2.2GHz CPU + 1*FPGA</li> <li>- 16GB DDR4 2666 for each LX2160A</li> <li>- 6GB DDR4 2666 for each accelerator FPGA</li> <li>- 600W 48V DC power with redundant input</li> <li>- 5 * fans with N:1 protection and speed control</li> <li>- Integrated BMC</li> </ul>	

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## Configuration

Configuration Type	Chassis	Modules
1	SC-60C (with CU module)	1 * DU module
2	SC-60C (with CU module)	2 * DU modules
3	SC-60A (without CU module)	1 * DU module
4	SC-60A (without CU module)	2 * DU modules

## Contact Information

### **CIG Shanghai Co., Ltd.**

5/F, Building 8, 2388 ChenHang Road

Shanghai, China 201114

Tel: +86-21-8023 3300

Email: sales\_5G@cigtech.com

www.cigtech.com

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