

5G Fronthaul Accelerator Card

Introduction

In the ORAN open architecture, the wireless RAN network consists of three parts: DU, HUB and RU, and software and hardware are also decoupled. The operation of software no longer requires dedicated hardware, but can run on a general x86 server.

The 5G Fronthaul Accelerator Card (fronthaul accelerator card) product is a dedicated product for 5G ORAN DU equipment.

The 5G fronthaul accelerator card is a small single -slot card that can be inserted into a standard x86 or non-x86 server to achieve the instant protocol processing performance required by DU equipment. Enable DU equipment to provide better 5G performance and services, while reducing overall system power consumption and costs.





SPECIFICATION

Compute Resources	Features
- Based on XilinxVU9P high-performance FPGA chip design	- Support 10/25GbE Synchronous Ethernet (ITU-T G.8262)
- Provide 52 GTY Serdes interfaces, the highest rate is 32.75Gbps	- Reserve IEEE 1588 fronthaul clock interface
- Provide 3.7 million logical units	- On-chip high stability TC and clock debounce circuit
- Provide 400Mb on-chip RAM	- Support: free-run, synchronous, holdover three modes
- Collect 6 PCIe cores	- Support fronthaul PTP HW Stamping
- Provide 11.9K DSP processing units, each unit can perform 27×	

18 digit calculation	
Memory	Size and Power consumption
- Support 2×4GB DDR4-2400 64b w/ECC memory by default	- Based on PCIe half length, half height, single slot design
- Support up to 10 DDR4 memory modules	- Size: 174.8mm×70.06mm×22.9mm
- Support 1Gb Nor Flash	- Power consumption: <75W (no external power supply required)
Fronthaul interface	PCIe interface
- Provides 2 SPF28 optical fiber interfaces, a single port supports a maximum rate of 25Gbps	- Provide 1 PCIe Gen3 x16 interface



Accelerator card appearance





